	Application No.	Applicant(s)	
Notice of Allowability	09/301,284	TAKAYAMA ET AL.	
	Examiner	Art Unit	
	Ted T. Vo	2122	
The MAILING DATE of this communication appears All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIOF the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED or other appropriate comn IGHTS. This application is	in this application. If not included nunication will be mailed in due co	urse. THIS
1. X This communication is responsive to 9/24/04.			
2. The allowed claim(s) is/are 2-11 and 49.			
3. The drawings filed on are accepted by the Examine	r.	•	
 4. Acknowledgment is made of a claim for foreign priority ur a) All b) Some* c) None of the: 1. Certified copies of the priority documents have 2. Certified copies of the priority documents have 3. Copies of the certified copies of the priority documents have International Bureau (PCT Rule 17.2(a)). * Certified copies not received: Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONM THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.	e been received. e been received in Applicat cuments have been receive	ion No ed in this national stage applicatio	
5. A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give			TICE OF
 CORRECTED DRAWINGS (as "replacement sheets") must (a) ☐ including changes required by the Notice of Draftspers 1) ☐ hereto or 2) ☐ to Paper No./Mail Date 8/07/0 (b) ☐ including changes required by the attached Examiner's Paper No./Mail Date Identifying indicia such as the application number (see 37 CFR 1 	son's Patent Drawing Revie 1. s Amendment / Comment on .84(c)) should be written on	or in the Office action of the drawings in the front (not the ba	ack) of
each sheet. Replacement sheet(s) should be labeled as such in to 7. DEPOSIT OF and/or INFORMATION about the deposit attached Examiner's comment regarding REQUIREMENT	sit of BIOLOGICAL MAT	TERIAL must be submitted. No	te the
 Attachment(s) 1. ☐ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☑ Information Disclosure Statements (PTO-1449 or PTO/SB/O Paper No./Mail Date 8/18/04 4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material 	6. ☐ Interview 9 Paper No 7. ☐ Examiner	nformal Patent Application (PTO- Summary (PTO-413), ./Mail Date s Amendment/Comment s Statement of Reasons for Allowa	

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1. The amendment filed on 9/24/04 has fully considered. The amendment of independent Claim 49 is fully responsive to the discussion held in an interview dated: 09/20/04, to result the difference, "upper bits which indicate a position of a processing packet in a memory", from prior art of record, Christie et al., US Patent No. 5,559,975.

Reasons for Allowance

2. Claims 2-11, 49 are allowed.

Prior art of record, Christie, discloses a processor that reads ROP instructions (four-bytes processing packet). Using a less significant program counter (counting bits are generated via a decoder) and a more significant program counter, the processor reads every byte of a ROP and reads all the ROPs through out the memory.

As pointed out by Applicants (Remarks, page 7) that Christie's lower bits (less significant program counter) are generated by a decoder, instructions stored in the memory are not indicated by the combination of the upper and lower bits.

Therefore, the following is an examiner's statement of reasons for allowance:

The cited prior arts taken alone or in combination fail to teach the claim to a processor for reading instructions comprising at least features:

"at least one processing packet having a natural number of bytes, the processing packet including a predetermined number of processing target instructions, each processing target instruction being an operation to be executed by the processor, the number of processing target instructions being any natural number except for a power of 2;

a first address indicator configured to indicate a storage position of the processing packet in the memory; and

a second address indicator configured to indicate a position of a processing target instruction within the processing packet by using the same number of positions as the predetermined number of

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processing target instructions, and cycling through the positions within the processing packet by an increment value" as recited in such manners in independent Claim 49.

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3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Ted T. Vo whose telephone number is (571) 272-3706. The examiner can normally be reached on 8:00AM to 5:30PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tuan Q. Dam can be reached on (571) 272-3694. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

WEİ Y. ZHEN PRIMARY EXAMINER

TTV
Patent Examiner
Art Unit 2122
November 24, 2004